



孕龍科技股份有限公司
Zeroplus Technology Co., Ltd.

SPECIFICATION

MODEL: LAP-SPI Compatible(Atmel Memory)-M

PART NO: _____

VERSION: V1.02

Approver		Check	Design
GM	PM		

Customer Confirm

*Please fax the file to Zeroplus Technology after signing.

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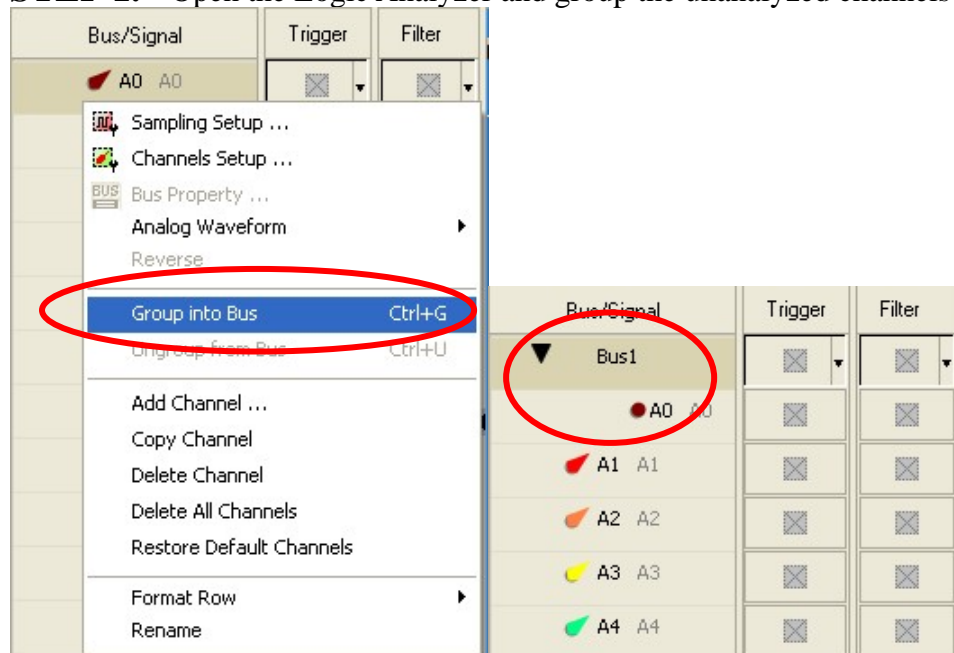
1 Software Register

Please register the software as the following steps:

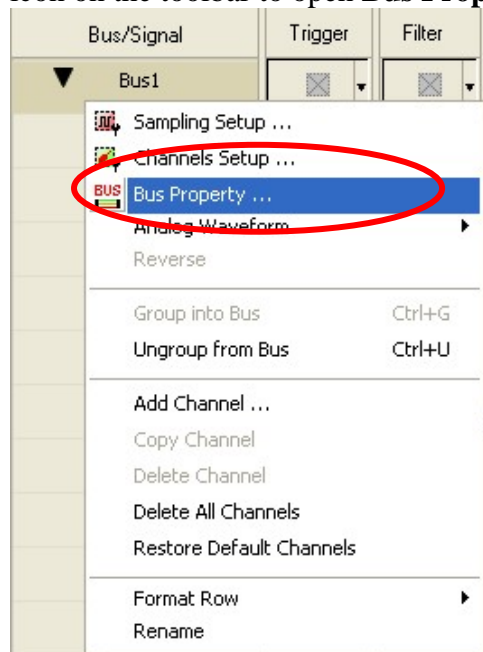
※ **Remark1:** The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.

※ **Remark2:** We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

STEP 1. Open the Logic Analyzer and group the unanalyzed channels into **Bus1** by pressing the **Right Key**.

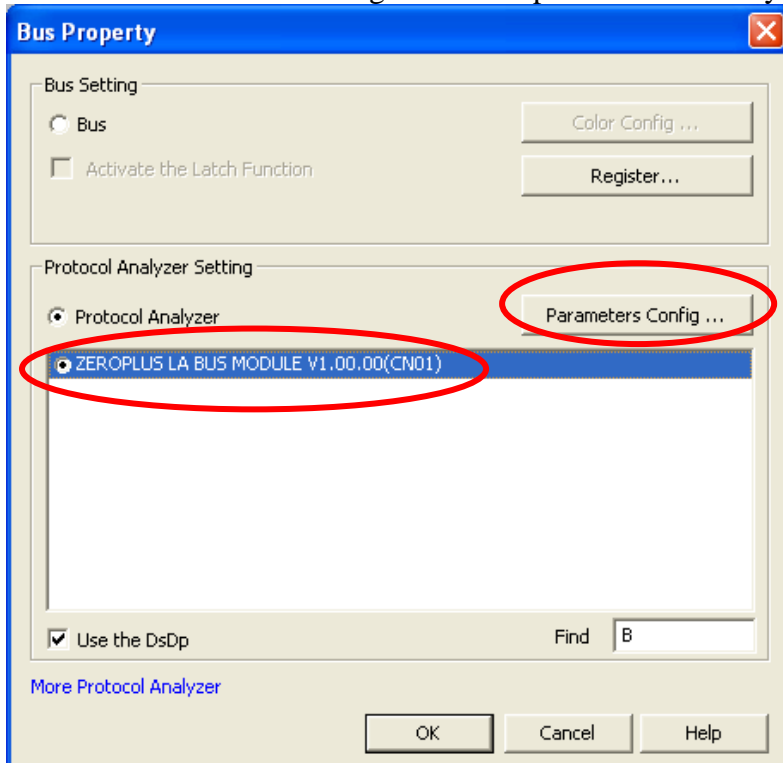


STEP 2. Select **Bus 1**, then press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus** icon on the toolbar to open **Bus Property** dialog box.

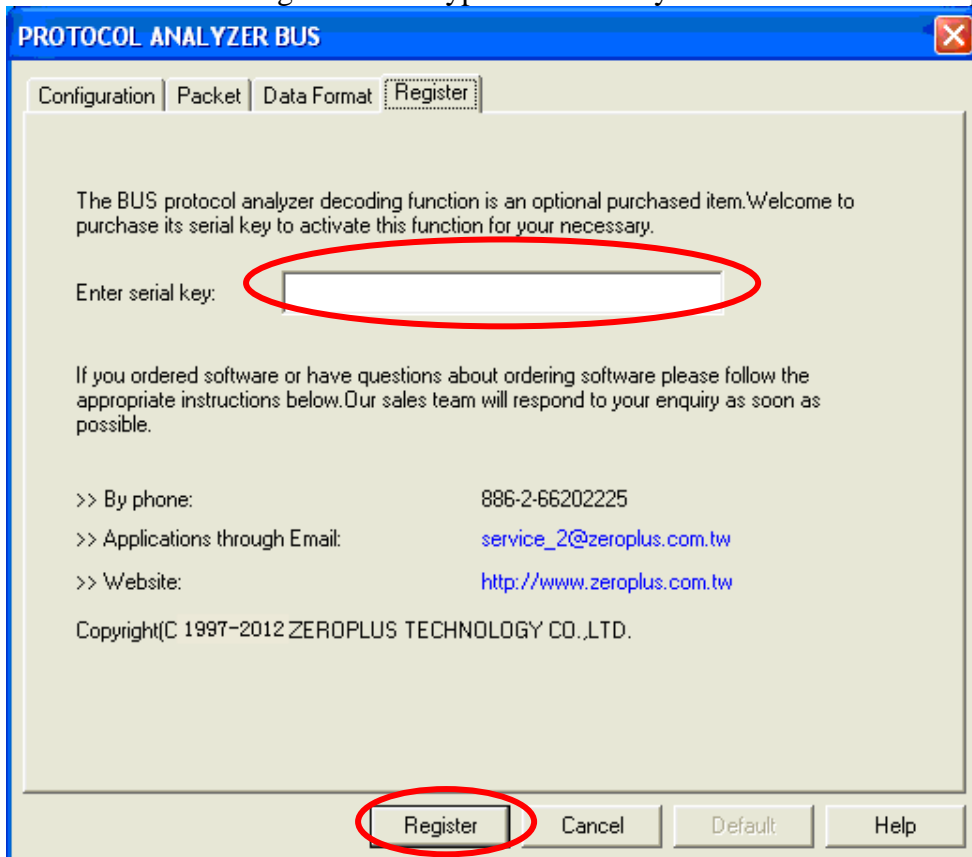




STEP 3. Select the Protocol Analyzer, and then choose **ZEROPLUS LA BUS MODULE V1.00.00 (CN01)**. Next click Parameters Configuration to open Protocol Analyzer Bus dialog box.

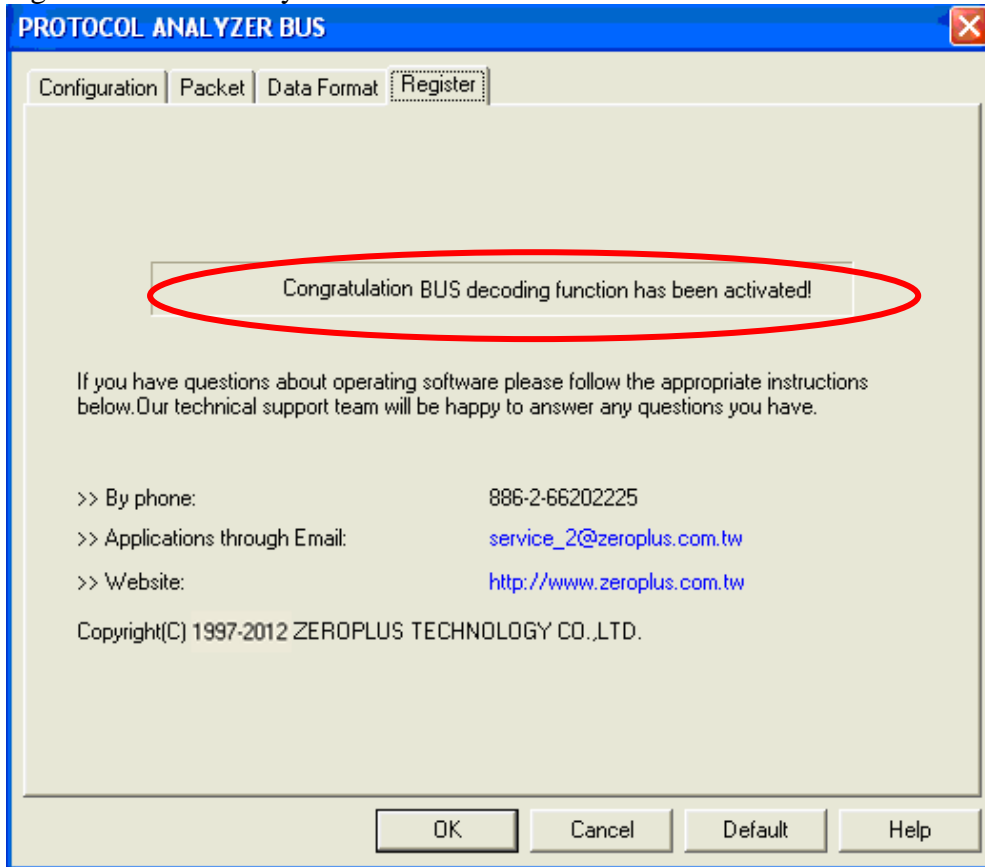


STEP 4. Press Register tab to type the serial key number of BUS. Then press Register.





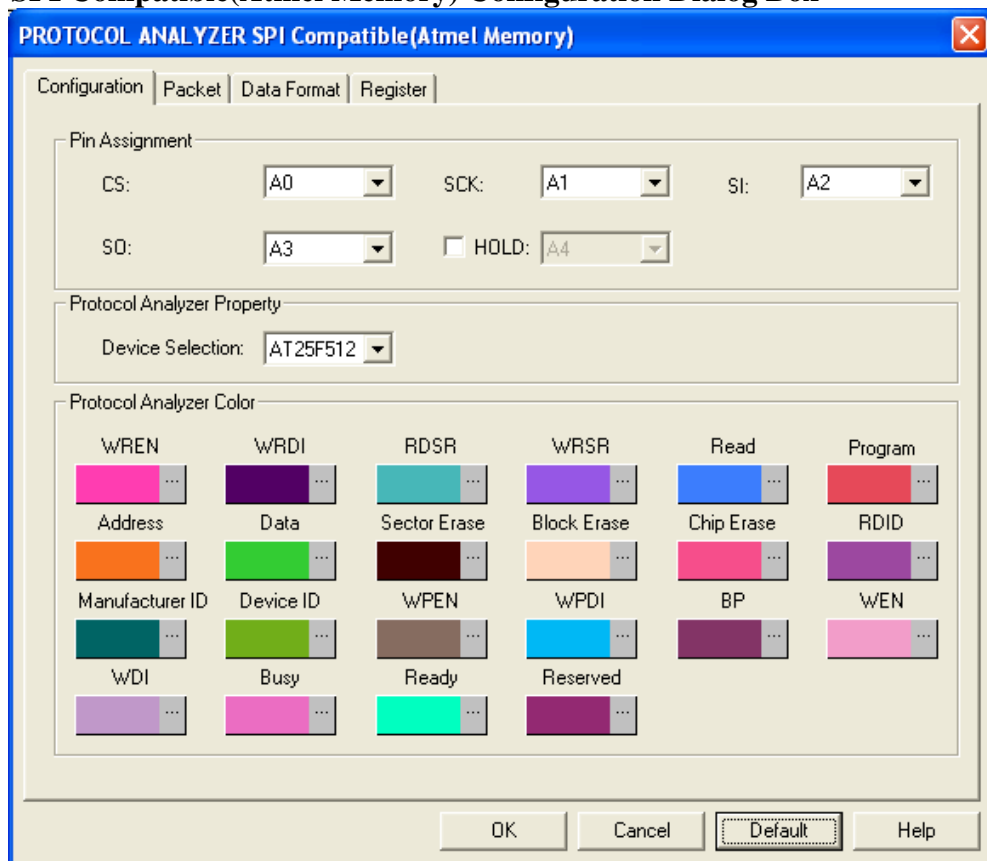
STEP 5. After pressing the Register button, following dialog box will appear, it denotes that the BUS has been registered successfully.



2 User Interface

In the configuration, please refer to below images to select options of setting SPI Compatible(Atmel Memory) module.

SPI Compatible(Atmel Memory) Configuration Dialog Box



Pin Assignment:

Four or five channels are required for Compatible(Atmel Memory) to decode the signal.

CS: It is the Chip Select channel and it is available for the Low Level.

SCK: It is the Serial Clock channel, which is triggered at the Rising Edge.

SI: It is the Command, Address and Data Input channel.

SO: It is the Data Output channel.

HOLD: It is the Data Transmission Hold channel, and it is available for the Low Level. Notice: It is not activated in general, which is denoted in grey. If it is activated, Compatible(Atmel Memory) will require 5 channels to decode; if it is not activated, Compatible(Atmel Memory) will require 4 channels to decode.

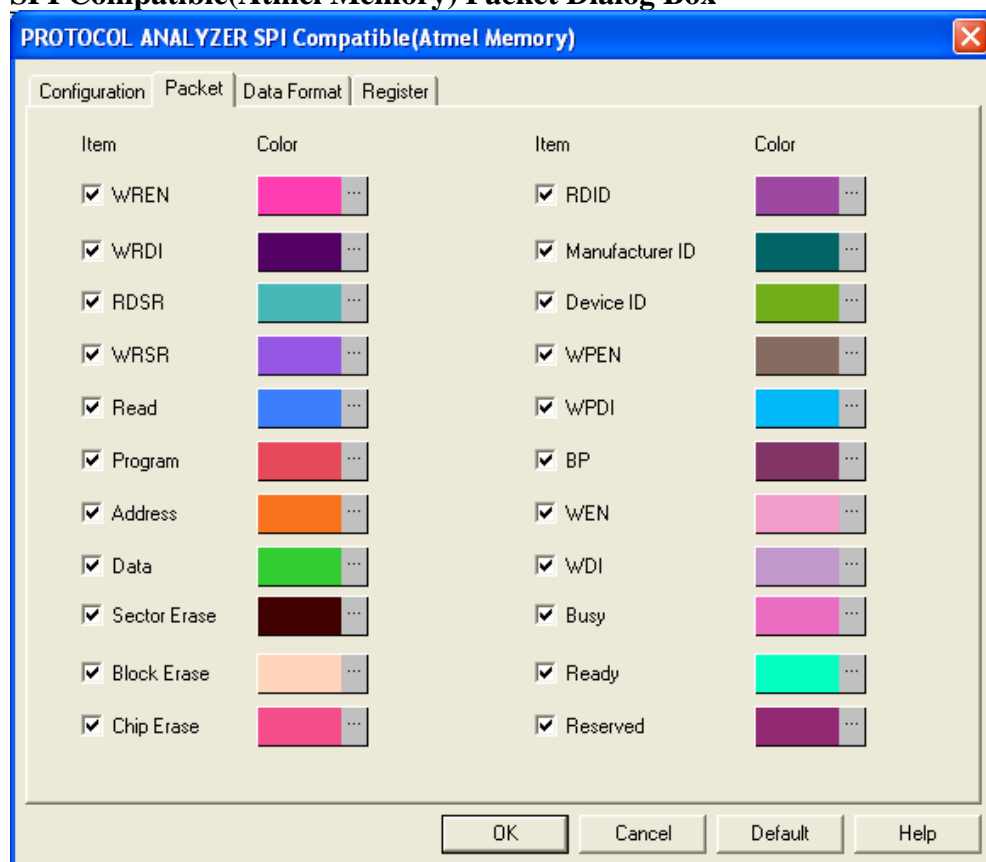
Protocol Analyzer Setting:

Device Select: There are six Devices for selecting that are AT25F512, AT25F1024, AT25F2048, AT25F4096, AT25FS010, AT25FS040, AT25010B, AT25020B and AT25040B. And the Default is AT25F512.

Protocol Analyzer Color:

The protocol analyzer colors can be varied by users.

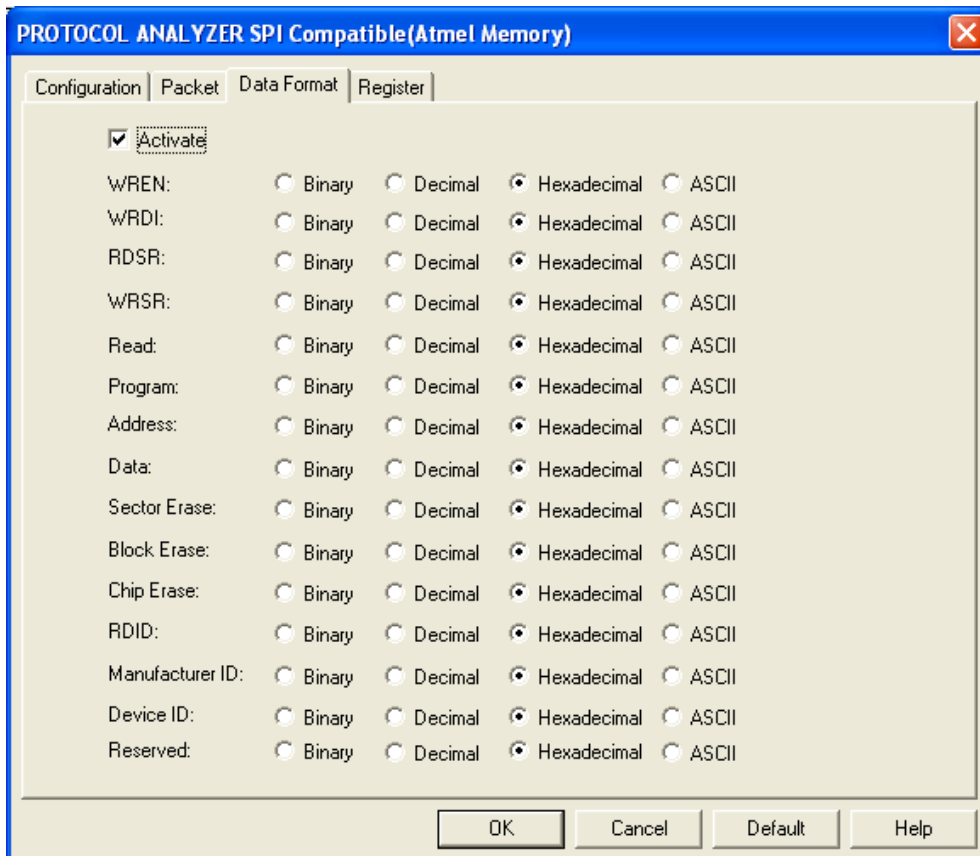
SPI Compatible(Atmel Memory) Packet Dialog Box



Item	Color	Item	Color
<input checked="" type="checkbox"/> WREN	magenta	<input checked="" type="checkbox"/> RDID	purple
<input checked="" type="checkbox"/> WRDI	dark purple	<input checked="" type="checkbox"/> Manufacturer ID	dark teal
<input checked="" type="checkbox"/> RDSR	teal	<input checked="" type="checkbox"/> Device ID	light green
<input checked="" type="checkbox"/> WRSR	purple	<input checked="" type="checkbox"/> WPEN	brown
<input checked="" type="checkbox"/> Read	blue	<input checked="" type="checkbox"/> WPDl	cyan
<input checked="" type="checkbox"/> Program	red	<input checked="" type="checkbox"/> BP	dark purple
<input checked="" type="checkbox"/> Address	orange	<input checked="" type="checkbox"/> WEN	pink
<input checked="" type="checkbox"/> Data	green	<input checked="" type="checkbox"/> WDI	light purple
<input checked="" type="checkbox"/> Sector Erase	dark red	<input checked="" type="checkbox"/> Busy	magenta
<input checked="" type="checkbox"/> Block Erase	light orange	<input checked="" type="checkbox"/> Ready	cyan
<input checked="" type="checkbox"/> Chip Erase	pink	<input checked="" type="checkbox"/> Reserved	purple

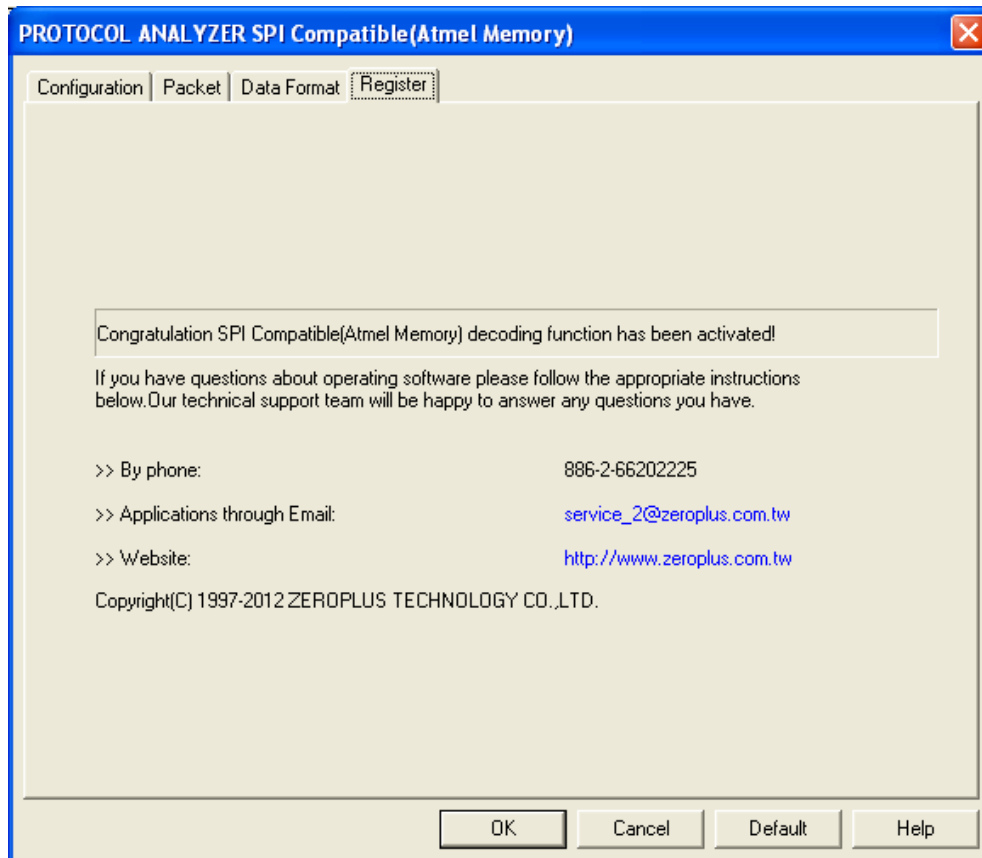
In the Packet dialog box, users can set the item to be displayed and the color of items.

SPI Compatible(Atmel Memory) Data Format Dialog Box



Users can set the Data Format of the WREN, WRDI, RDSR, WRSR, Read, Program, Address, Data, Sector Erase, Block Erase, Chip Erase, RDID, Manufacturer ID, Device ID and Reserved as their requirements. When selecting the option, Activate, the data format is decided by the settings in the Protocol Analyzer; when not selecting the option, Activate, the data format is decided by the settings in the main program.

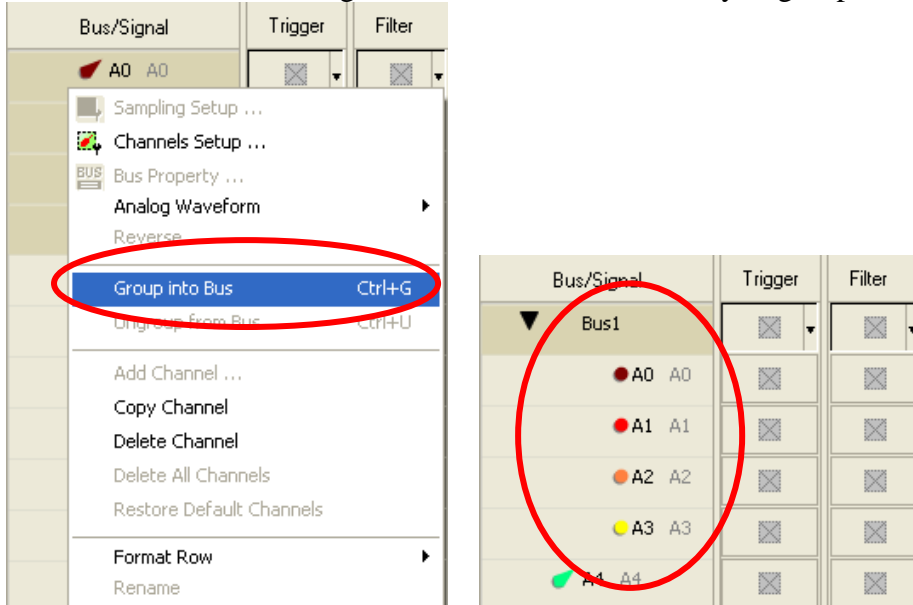
SPI Compatible(Atmel Memory) Register Dialog Box



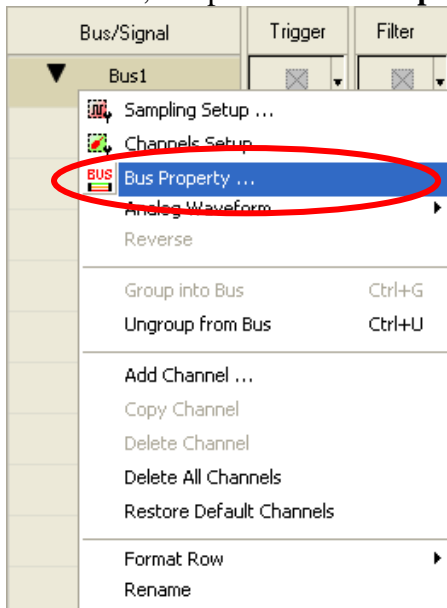
There is ZeroPlus company information. If you have any questions about software operations, you can contact ZeroPlus by Telephone or Email.

3 Operating Instructions

STEP 1. Group A0-A3 into **Bus1** by pressing the **Right Key** on the mouse. SPI Compatible(Atmel Memory) needs four channels to decode signals at least, so it is necessary to group four or more channels into a Bus.

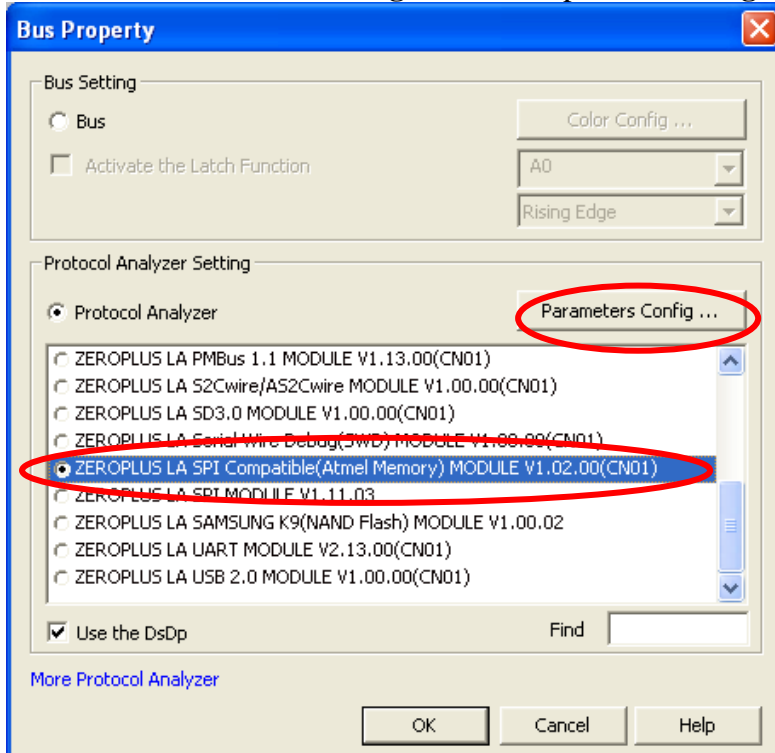


STEP 2. Select **Bus1**, press right key and select **Bus Property** from the popped menu, or click the **Bus** icon on the toolbar, to open the **Bus Property** dialog box.

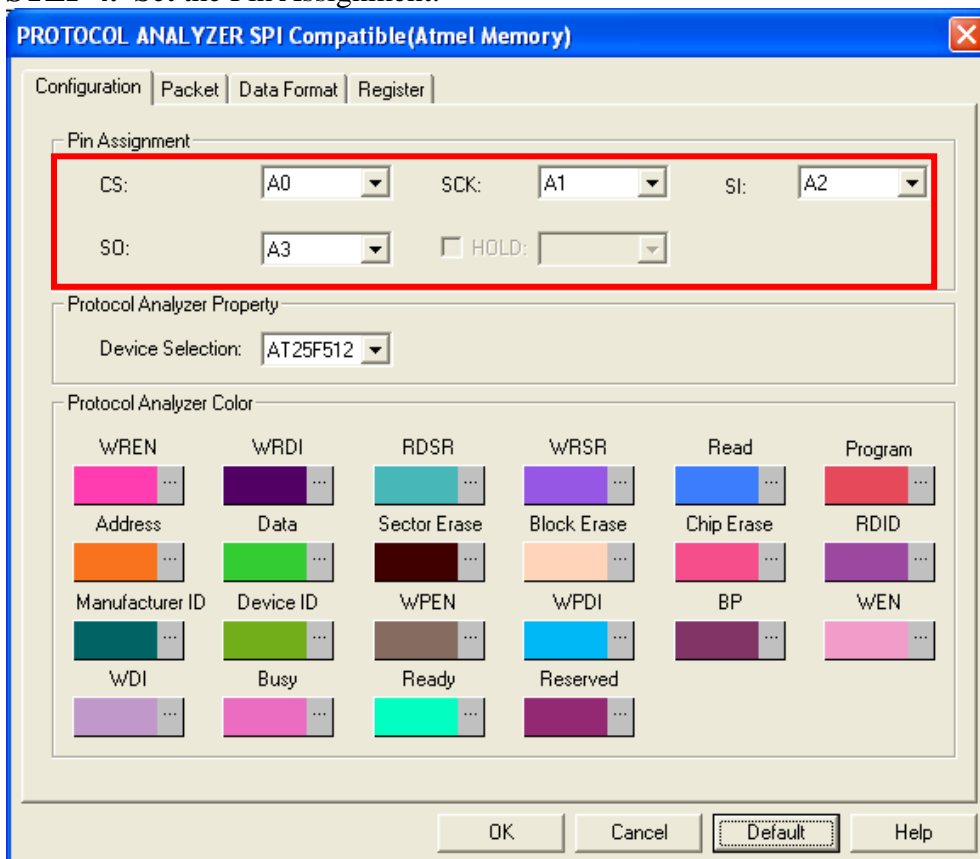




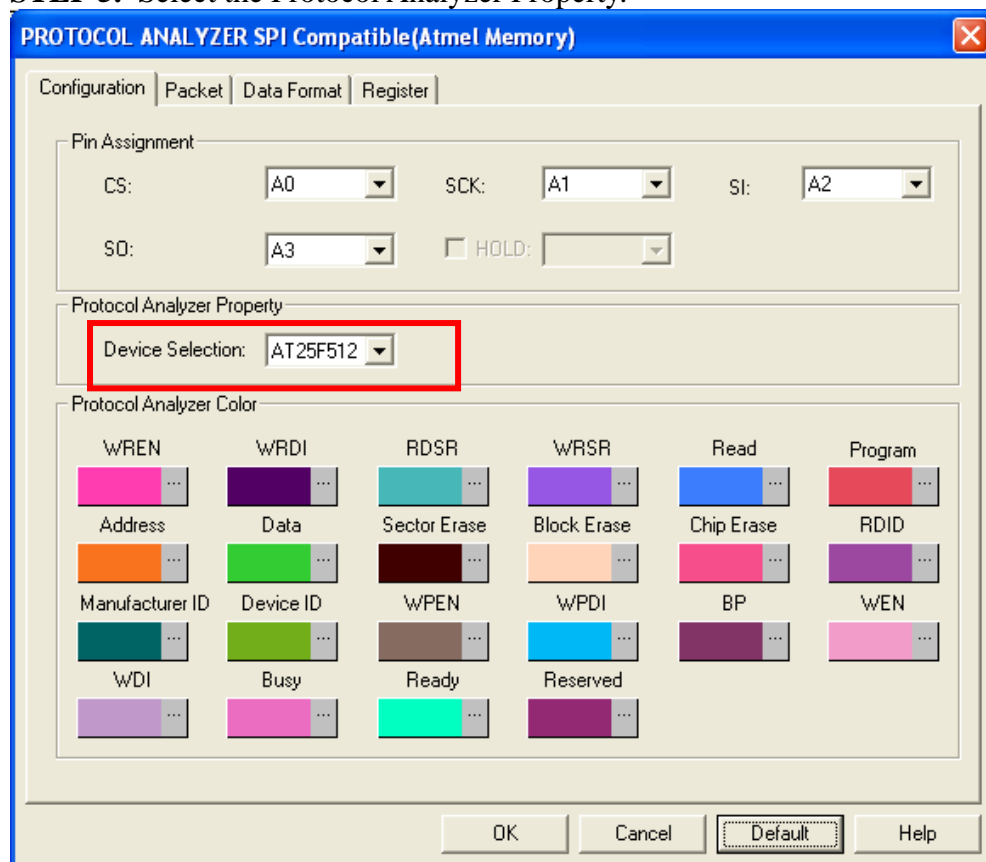
STEP 3. For Protocol Analyzer SPI Compatible(Atmel Memory) Parameters Configuration, select Protocol Analyzer, and then choose **ZEROPLUS LA SPI Compatible(Atmel Memory) MODULE V1.02.00(CN01)**. Next click **Parameters Configuration** to open the **Configuration** dialog box.



STEP 4. Set the Pin Assignment.



STEP 5. Select the Protocol Analyzer Property.



STEP 6. Set the Protocol Analyzer Color.

